



HSK120N10

100V N-Channel MOSFET

● Features:

■ 120A, 100V, $R_{DS(on)(Typ)} = 5.0m\Omega @ V_{GS}=10V$

■ Low Gate Charge

■ Low C_{rss}

■ 100% Avalanche Tested

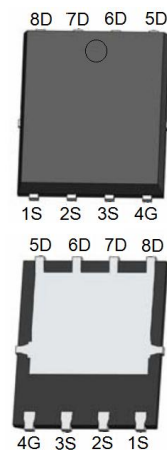
■ Fast Switching

■ Improved dv/dt Capability

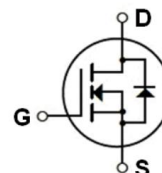
● Application:

■ High Frequency Switching Mode Power Supply

■ Active Power Factor Correction



DFN5*6



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DSS}	Drain-Source Voltage	100	V
I_D	Drain Current - Continuous ($T_c=25^\circ\text{C}$, Silicon limit)	138	A
	Drain Current - Continuous ($T_c=25^\circ\text{C}$, Package limit)	120	A
	Drain Current - Continuous ($T_c=100^\circ\text{C}$, Silicon limit)	87	A
I_{DM}	Drain Current - Pulsed	480	A
V_{GSS}	Gate-Source Voltage	± 20	V
E_{AS}	Single Pulsed Avalanche Energy (Limit Reference Value) (Note5)	231	mJ
P_D	Power Dissipation ($T_c = 25^\circ\text{C}$) -Derate above 25°C	139	W
		1.11	W/ $^\circ\text{C}$
T_j	Operating Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case (Note2)	0.90	$^\circ\text{C} / \text{W}$

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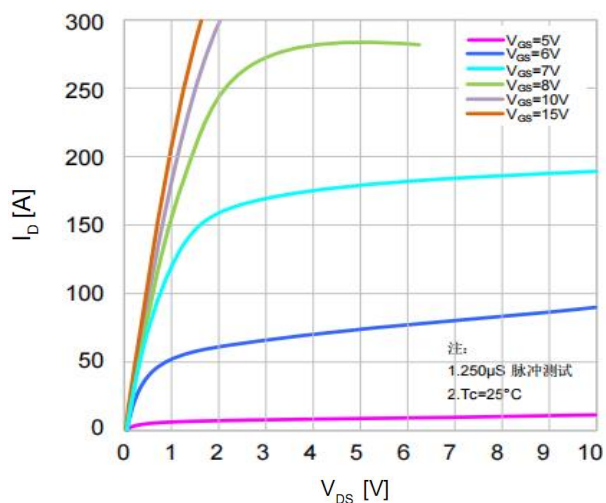
Electrical Characteristics(Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditons	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-source Breakdown Voltage	V _{GS} =0V ,I _D =250μA	100	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V,V _{GS} =0V	--	--	1	μA
I _{GSSF}	Gate-Body Leakage Current,Forward	V _{GS} =+20V, V _{DS} =0V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current,Reverse	V _{GS} =-20V, V _{DS} =0V	--	--	-100	nA
On Characteristics (Note3)						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =250μA	2.0	3.0	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} =10 V, I _D =50A	--	5.0	5.5	mΩ
Dynamic Characteristics (Note4)						
C _{iss}	Input Capacitance	V _{DS} =40V,V _{GS} =0V, f=1.0MHz	--	3810	--	pF
C _{oss}	Output Capacitance		--	649	--	pF
C _{rss}	Reverse Transfer Capacitance		--	25.7	--	pF
Switching Characteristics (Note4)						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 40 V, I _D =13.3 A, R _G =24 Ω, V _{GS} =10V	--	48	--	ns
t _r	Turn-On Rise Time		--	74	--	ns
t _{d(off)}	Turn-Off Delay Time		--	155	--	ns
t _f	Turn-Off Fall Time		--	86	--	ns
Q _g	Total Gate Charge	V _{DS} = 80 V, I _D =50A, V _{GS} = 10 V	--	64	--	nC
Q _{gs}	Gate-Source Charge		--	23	--	nC
Q _{gd}	Gate-Drain Charge		--	17	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current (Note2)		--	--	120	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	480	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} =0V,I _S =50A (Note3)	--	--	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =20A, d I _F /dt=100A/μs (Note3)	--	65	--	ns
Q _{rr}	Reverse Recovery Charge		--	148	--	nC

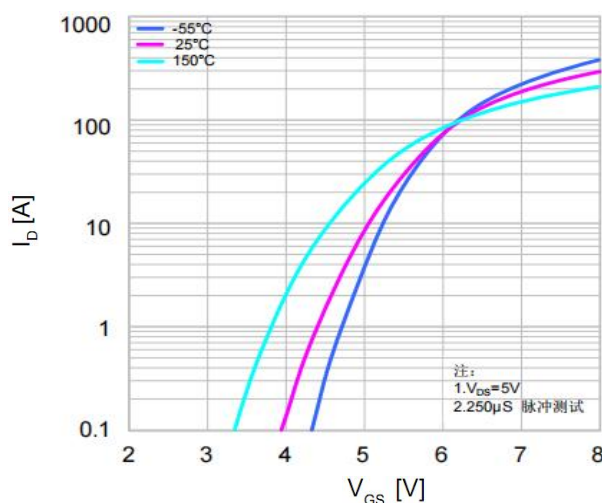
Notes:

- 1、Repetitive Rating:Pulse Width Limited by Maximum Junction Temperature.
- 2、Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3、Pulse Test : Pulse Width ≤300 μ s, Duty Cycle≤2%.
- 4、Guaranteed by design, not subject to production.
- 5、EAS condition: L = 0.5mH, I_{AS} =23A, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25°C.

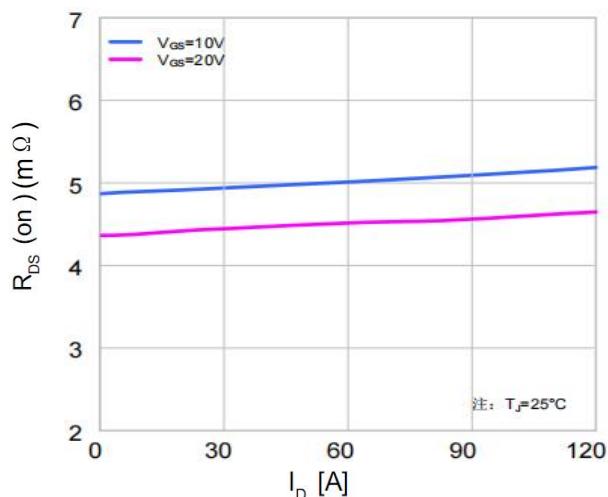
On-Regin Characteristics



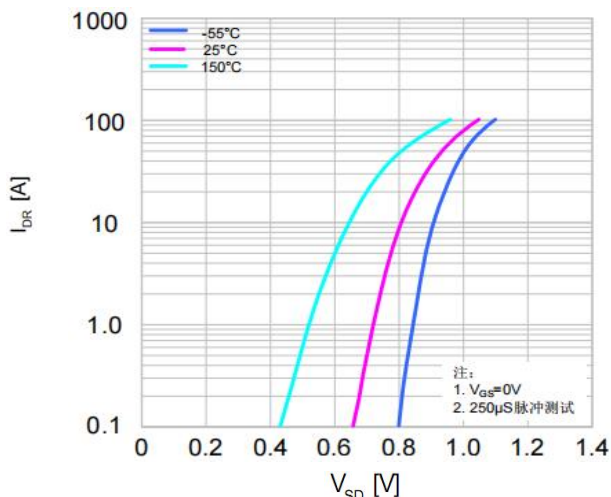
Transfer Characteristics



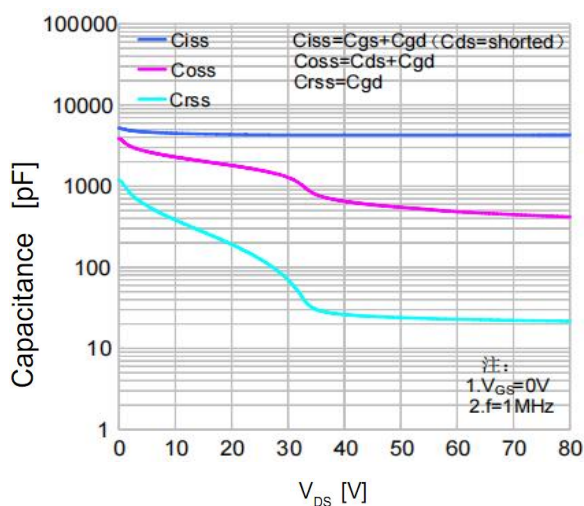
On-Resistance Variation vs. Drain Current and Gate Voltage



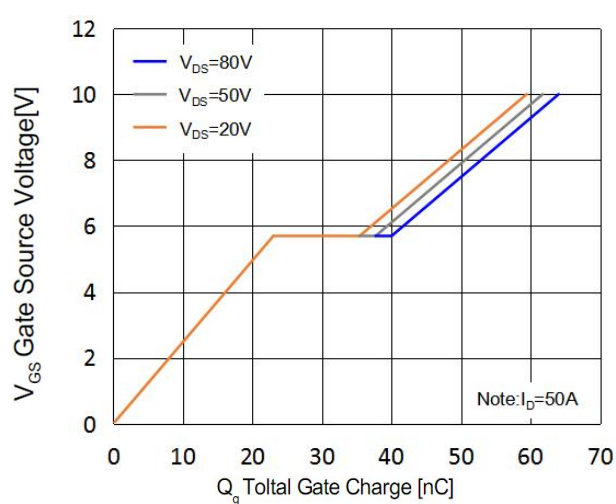
Body Diode Forward Voltage Variation vs. Source Current and Temperature



Capacitance Characteristics



Gate Charge Characteristics

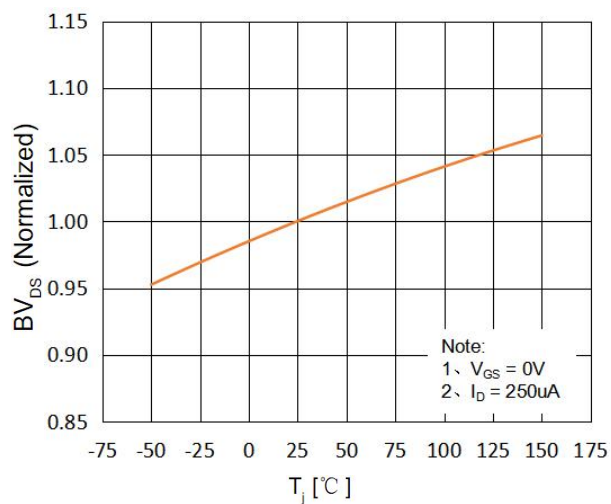




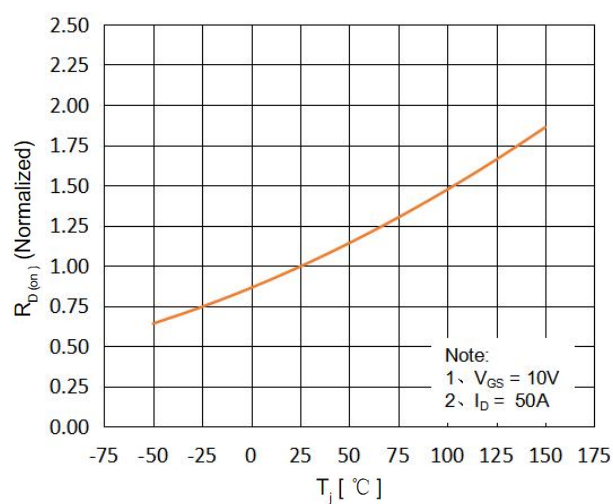
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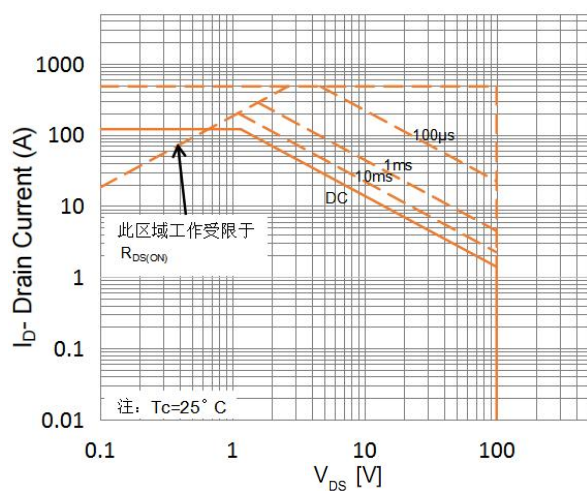
Breakdown Voltage Variation vs. Temperature



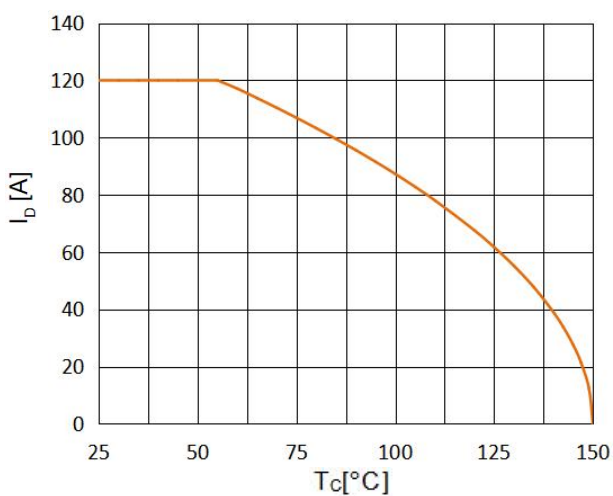
On-Resistance Variation vs. Temperature



Maximum Safe Operating Area



Maximum Drain Current Vs. Case Temperature





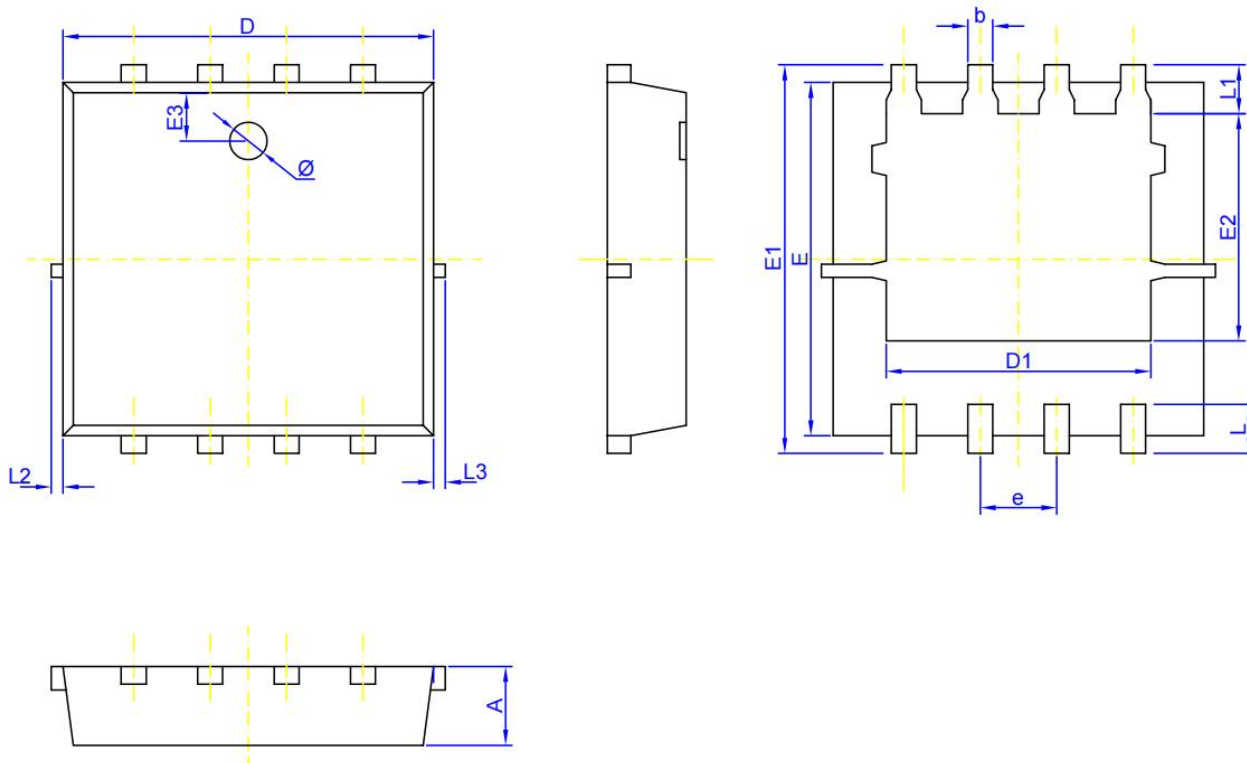
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DFN5*6-8L Package Dimensions

UNIT: mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	0.90	1.00	1.10	b	0.25	0.30	0.35
D	4.95	5.05	5.15	e	1.22	1.27	1.32
D1	4.21	4.41	4.61	L	0.585	0.685	0.785
E	5.65	5.85	6.05	L1	0.525	0.625	0.725
E1	5.95	6.15	6.35	ϕ	1.00	1.20	1.40
E2	3.55	3.75	3.95	L2	0~0.10		
E3	0.90	1.10	1.30	L3	0~0.10		



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注意事项：

- 1、在电路设计时请不要超过器件的最大额定值，否则会影响整机的可靠性。
- 2、MOSFET产品为静电敏感型器件，使用时应注意采取防静电保护措施，如佩戴防静电手环、设备接地等。
- 3、如需安装散热片，请注意控制扭力大小及散热片的平整度。
- 4、该规格书由华科公司制作，并可能不定期更改，恕不另行通知。
- 5、如有疑问，请及时联系我司销售代表。

版本履历表：

序号	版本号	修改时间	修改记录
1	V1.0	2022-12-9	首次发行